

Memorandum of Understanding

Between



VLSI Society of India (VSI)

AND



**Jaypee University of Engineering and
Technology, Guna (M.P)**

March 18, 2024



मध्य प्रदेश MADHYA PRADESH

CC 209745

This Memorandum of Understanding ("MoU") entered into, at Indian Institute of Technology, Indore, India on **March 18, 2024** by and between:

"VLSI Society of India (VSI)", a society promoting VLSI and Semiconductor Education and Research in India, Represented by Dr. Satya Gupta, President VSI

AND

"Jaypee University of Engineering and Technology, Guna, its address at A-B Road, Raghogarh, District Guna-473226 (MP) - hereinafter referred to as "JUET-Guna" Represented by Prof Devendra Kumar Rai, Vice-Chancellor, JUET-Guna:

Scope and Purpose:

This MoU is being entered between VSI and JUET-Guna to define a partnership that will work towards developing Talent, Research, Start-ups and Employment Opportunities in the areas of VLSI Design and Semiconductors. Both the participating organizations will work together in collaboration to achieve the following objectives:

[Signature]
Kalita Prasad Sharma
Member / Secretary
Raghogarh, Guna (M.P.)

[Signature]

[Signature]

VSI has more than 5000+ semiconductor professionals and volunteers around the world representing full semiconductor supply chain. Working in collaboration with JUET-Guna, the partnership will help in holistic growth of semiconductor ecosystem for the Madhya Pradesh region to build the required talent pipeline for the current and future growing needs that the India Semiconductor Industry is witnessing.

2. VSI and JUET-Guna will work together in developing and enhancing the VLSI Design and Semiconductors curriculum at undergraduate, post graduate and doctorate level and make these programs relevant for current and future needs of the industry. VSI will also provide help in implementing these courses.
3. VSI will provide help in setting up the required lab infrastructure for conducting the courses and projects in an effective manner.
4. VSI shall support through their industry volunteer network in providing expert lectures, projects, internship opportunities and mentoring the JUET-Guna students who are taking these courses at undergraduate, post-graduate or doctorate level.
5. VSI and JUET-Guna will collaborate in promoting and helping Research activities in VLSI Design & Semiconductors domains for current and future technology needs and trends.
6. VSI and JUET-Guna will work together for faculty development, training and enhancing knowledge of the faculty members in VLSI Design and Semiconductor Areas.
7. VSI and JUET-Guna will jointly hold workshops and training programs for students and faculty members from JUET-Guna and other institutions in the Madhya Pradesh Region.
8. VSI and JUET-Guna will work together to promote awareness regarding Electronics and Semiconductors and the career opportunities among the high school students and parents so that the students give preference to these areas while choosing their engineering branches.
9. JUET-Guna will provide help with VSI Madhya Pradesh Regional chapter in promoting VLSI Design and Semiconductors in the region, help organizing and participating in VSI organized conferences, workshops, and events.

This MoU shall be effective from the date of its execution, **March 18, 2024** and shall remain in force for a period of 5 years. The parties may extend the term in writing henceforth. The MoU may be terminated by either party by giving a written notice of 30 days to the other party, without assigning any reason thereof.

Parties have agreed to execute this MOU to deliver specific objectives on best efforts basis only with no financial transactions involved. Financial obligations arise/involved, if any, will be addressed through separate agreement/Contract.

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Witness whereof, the parties hereto have offered their signature from the date mentioned above.

Authorized Signatory for VSI

Saty Gupta

Dr. Satya Gupta
President, VLSI Society of India

Authorized Signatory of JUET-Guna

D.K. Rai

Prof. D.K. Rai ^{18/3/24}
Vice-Chancellor,
Jaypee University of Engineering and
Technology, Guna ^{473226 (M.P)}
India Jaypee University of Engineering & Technology
Raghogarh, Guna (M.P)

Witness 1

Chitra
CHITRA HARIHARAN
Secretary - VSI

Witness 1

S. Garg
18-3-24
Dr. S. Garg
Dean (IR), JUET, Guna



D.K. Rai
Saty Gupta
I, *Saty Gupta*, do hereby certify that the above mentioned parties have signed this affidavit voluntarily and in full view of me and have been administered the oath by me. I have examined the contents of the affidavit and find it to be true and correct. I have also explained the contents of the affidavit to the deponent and he has understood the same and has explained the same to me. I have also explained the contents of the affidavit to the deponent and he has understood the same and has explained the same to me. I have also explained the contents of the affidavit to the deponent and he has understood the same and has explained the same to me.

K.P. Sharma
K.P. Sharma
Notary Public
Teha Raipur, Distt. Guna (M.P.)